

IN THE UNITED STATES
PATENT AND TRADEMARK OFFICE

CONTINUATION APPLICATION OF:)
LARRY F. WEBER, ET AL..)
FOR: POWER EFFICIENT)
SUSTAIN DRIVERS)
AND ADDRESS DRIVERS)
FOR PLASMA PANEL)
FILED: Herewith)
PRIOR APPLICATION:)
Serial No.: 06/911,396)
Filed: September 25, 1986)
Examiner: Michael Razavi)
Group Art Unit: 266)

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of
Patents and Trademarks
Washington, D.C. 20231

Sir:

Copies of the following identified and correspondingly
marked publications are submitted herewith:

A. H. Tottori, E. Hatabe, F. Isogai and S. Yoshida, "A
Driving Circuit for Plasma Display Panels", Society For
Information Display, SID 75 Digest, Vol. 6, pp. 118-119, April,
1975;

B. W.E. Johnson, E.A. Oster and H.J. Hoehn, "Plasma
Display/Memory Panel with Integral Drive Circuitry", Society For
Information Display, SID 77 Digest, Vol. 8, pp. 20-21, April ,
1977;


C. M.L. Higgins, "A Low-Power Drive Scheme for AC TFEL
Displays", Society For Information Display, SID 85 Digest,
Vol. 16, pp. 226-228, April-May, 1985.

The Tottori, et al article describes the use of bipolar transistors with added diodes for driving a plasma display panel. N-type bipolar transistors are used to drive one side of the plasma display panel matrix and P-type bipolar transistors are used to drive the other side of the panel matrix.

The Johnson article describes the use of a MOFSET device with two added diodes per display line for addressing a plasma display panel. Respective N and P-type MOSFETs are used for driving respective sides of the plasma display panel line electrode matrix.

The Higgins article is referred to on page 20 of the above-identified application. Higgins describes the use of an inductor as part of a L-C resonant circuit for driving an electroluminescent panel.

Respectfully submitted,


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